

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

SHAFQAT AHMED, ET AL.

Application No.: 10/619,961

Filed: July 15, 2003

For: **"Method of Forming Transistor Having
Insulating Spacers On Gate Sidewalls"**

Art Group: 2812

Examiner: Booth, Richard A.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Reexamination Continuation Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

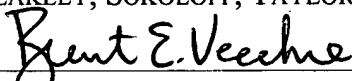
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Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

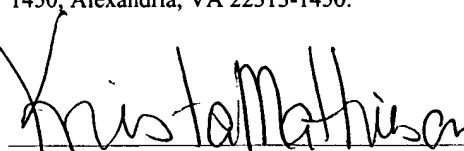
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: August 22, 2005


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

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of

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Complete if Known

Application Number 10/619,961
Filing Date July 15, 2003
First Named Inventor Shafqat Ahmed
Art Unit 2812
Examiner Name Booth, Richard A.
Attorney Docket Number 42P13230D

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code² (if known)			
		US-5,834,346	11-10-1998	Sun, et al.	
		US-5,937,301	08-10-1999	Gardner, et al.	
		US-6,103,611	08-15-2000	En, et al.	
		US-4,745,086	05-17-1998	Parrillo, et al.	
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		US-4,728,617	03-01-1988	Woo, et al.	
		US-4,818,714	04-04-1989	Haskell	
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		US-5,102,816	04-07-1992	Manukonda, et al.	
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		US-5,171,700	12-15-1992	Zamanian	
		US-5,328,862	07-12-1994	Goo	
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		US-			
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T³
		Country Code³ - Number⁴ - Kind Code⁵ (if known)				
		EP0195607	09-24-1986	FUJITSU LTD.		
		EP0439173	07-31-1991	NIPPON ELECTRIC CO.		
		DE3709708	10-08-1987	TOSHIBA KAWASAKI KK.		
		JP2219237	08-31-1990	SHARP CORP.		
		JP60234367	11-21-1985	HITACHI LTD.		
		JP63066967	03-25-1988	HITACHI LTD.		
		JP2161733	06-21-1990	RICOH CO. LTD.		
		JP3272145	12-03-1991	MATSUSHITA ELECTRON CO.		

Examiner
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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